

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	2	"0829812".did.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L5	0	"EP-0829812\$".did.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L6	0	"00829812".did.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L7	4779	ep-0\$.did.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L8	18438	micro processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L9	11450	micro-processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L10	506970	microprocessor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06

L11	513156	microprocessor or micro-processor	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L12	517534	microprocessor or micro-processor or micro processor	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L13	517534	microprocessor or micro processor	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L14	0	"ep 0".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L15	0	"EP-829812\$".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L16	2954931	"EP-\$".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L17	0	"EP-00829812\$".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L18	3805	(convert or implement) with hardware with software with function \$5	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06

L19	32	(convert) adj4 software adj4 hardware	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L20	2	"5870588".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L21	0	Method of designing an integrated circuit and integrated circuit designed by such method	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L22	0	"EP-829812\$".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L23	0	"EP-0829812\$".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L24	26	(integrated circuit designed).ti.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L25	0	"EP-829812".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L26	0	"EP-829812-\$".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06

L27	2	"EP 829812\$".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
L28	19180	"716".clas. 703/13-22.ccls.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/12 16:06
S1	2	"0829812".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:54
S2	0	"EP-0829812\$".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:54
S3	0	"00829812".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:55
S4	4779	ep-0\$.did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:55
S5	17207	micro processor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:55
S6	10707	micro-processor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:55

S7	474272	microprocessor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:56
S8	479954	microprocessor or micro-processor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:56
S9	483961	microprocessor or micro-processor or micro processor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:56
S10	483961	microprocessor or micro processor	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:56
S11	0	"ep 0".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 09:56
S12	0	"EP-829812\$".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 10:00
S13	2866955	"EP-\$".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 10:00
S14	0	"EP-00829812\$".did.	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 10:01

S15	3439	(convert or implement) with hardware with software with function \$5	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 10:35
S16	30	(convert) adj4 software adj4 hardware	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 10:36
S17	2	"5870588".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/15 11:19
S18	0	Method of designing an integrated circuit and integrated circuit designed by such method	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:12
S19	0	"EP-829812\$".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:14
S20	0	"EP-0829812\$".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:14
S21	26	(integrated circuit designed).ti.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:14
S22	0	"EP-829812".did.	US_PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:17

S23	0	"EP-829812-\$".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:18
S24	2	"EP 829812\$".did.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 10:18
S28	17504	"716".clas. 703/13-22.ccls.\	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 11:24
S29	17504	"716".clas. 703/13-22.ccls.\	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 11:24
S30	17504	"716".clas. 703/13-22.ccls.	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/02/18 11:24

9/12/2008 4:09:36 PM

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